

Through Hole Lamp Product Data Sheet LTL-R42M12NH51

**Spec No.: DS20-2015-0268** Effective Date: 12/02/2015 Revision: -



BNS-OD-FC001/A4

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Through Hole Lamp LTL-R42M12NH51

# **Through Hole Lamp**

## LTL-R42M12NH51

<u>Rev</u>	<b>Description</b>	<u>By</u>	<u>Date</u>
P001	Preliminary Specification (RDR-20130408-01)	Javy H.	05/12/2015
P002	Update pin length specification	Javy H.	07/23/2015
	Above data for PD and Customer track	ing only	
-	New Specification	Javy. H.	11/30/2015





## Through Hole Lamp LTL-R42M12NH51

### 1. Description

CBI (Circuit Board Indicator) is a black plastic right angle Holder (Housing) which mates with Lite-On LED lamps. Lite-On CBI is available in a wide variety of packages, including top-view (Spacer) or right angle and horizontal or vertical arrays which is stackable and easy to assembly.

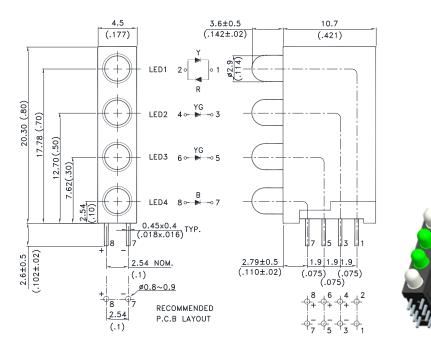
#### 1.1. Features

- Designed for ease in circuit board assembly.
- Black case enhance contrast ratio.
- Low power consumption & High efficiency.
- Lead free product & RoHS Compliant.
- T-1 lamp: emitted colors are red/green, yellow green and InGaN blue color.

#### **1.2. Applications**

- Computer
- Communication
- Consumer
- Industrial

### **2. Outline Dimensions**



#### Notes :

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is ±0.25mm (.010") unless otherwise noted.
- 3. The Holder (Housing) material is plastic black.
- 4. LED1 is red/yellow bi-color with white diffused Lens; LED2,3 are yellow green color with green diffused Lens; LED4 is blue color with white diffused Lens.
- 5. Specifications are subject to change without notice.



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### 3. Absolute Maximum Ratings at TA=25 $^\circ\!\!\mathbb{C}$

Parameter	Red	Yellow	Yellow Green	Blue	Unit		
Power Dissipation	52	52	52	117	mW		
Peak Forward Current							
(Duty Cycle $\leq$ 1/10, Pulse Width $\leq$ 0.1ms)	60	60	60	100	mA		
DC Forward Current	20	20	20	20	mA		
Operating Temperature Range	-40°C to + 85°C						
Storage Temperature Range	-40°C to + 100°C						
Lead Soldering Temperature							
[2.0mm (.079") From Body]	260°C for 5 Seconds Max.						

### 4. Electrical / Optical Characteristics at TA=25 $^\circ\!\mathrm{C}$

Parameter	Symbol	LEI	Color LED1 2,3; LED4 Min.		in.	Тур.		Max.		Unit	Test Condition		
		Red	Yellow	5	0	110		240			LED1,4 IF=20mA		
Luminous Intensity	IV	Green	Blue	8.7	180	19	400	50	880	mcd	LED2,3 IF=10mA Note 1,4		
	201/2	Red	Yellow			1(	100						
Viewing Angle	201/2	Green	Blue			100 60			deg		Note 2 (Fig.6)		
Peak Emission Wavelength	λP	Red	Yellow			632 591			nm		Measurement		
Feak Emission wavelength		Green	Blue			572	468	468			@Peak (Fig.1)		
Deminent Weyelength	<u>ک</u> ط	Red	Yellow	617	583	624	589	632	596		LED1,4 IF=20mA LED2,3 IF=10mA		
Dominant Wavelength	λd	Green	Blue	566	460	569	470	574	475	nm	Note 3		
Spectral Line Half Width	Δλ	Red	Yellow			20		20		- r			
Spectral Line Half-Width		Green	Blue			15	20	nm					
Forward Valtage	VF	Red	Yellow			2.1		2.6		V	LED1,4 IF=20mA		
Forward Voltage		Green	Blue			2.0	3,2	2.6	3.8	v	LED2,3 IF=10mA		
Reverse Current	IR	Red	Yellow					1(	00	μA	VR = 5V, Note 6		
		Green	Blue						0	μΑ	VIX = 3V, NOLE 0		

#### NOTE:

- 1. Luminous intensity is measured with a light sensor and filter combination that approximates the CIE eye-response curve.
- 2.  $\theta$ 1/2 is the off-axis angle at which the luminous intensity is half the axial luminous intensity.
- 3. The dominant wavelength,  $\lambda d$  is derived from the CIE chromaticity diagram and represents the single wavelength which defines the color of the device.
- 4. Iv guarantee must be included with  $\pm 30\%$  testing tolerance.
- 5. Reverse current is controlled by dice source.
- 6. Reverse voltage (VR) condition is applied for IR test only. The device is not designed for reverse operation.



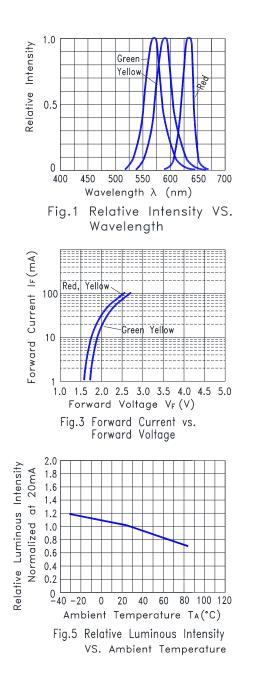


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### 5. Typical Electrical / Optical Characteristics Curves

(25°C Ambient Temperature Unless Otherwise Noted)

<for Red, Yellow, Yellow Green>



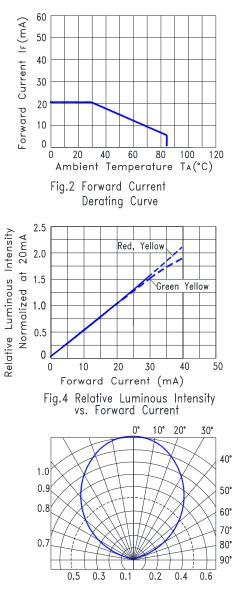


Fig.6 Spatial Distribution

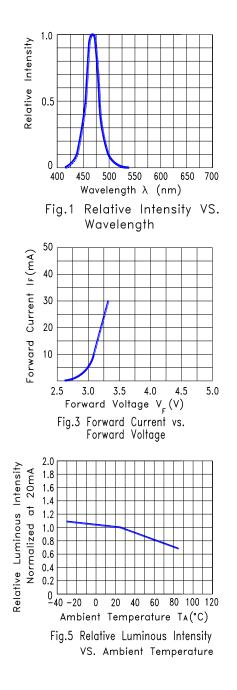




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(25°C Ambient Temperature Unless Otherwise Noted)

<for Blue color>



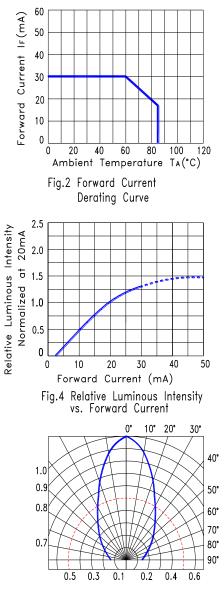
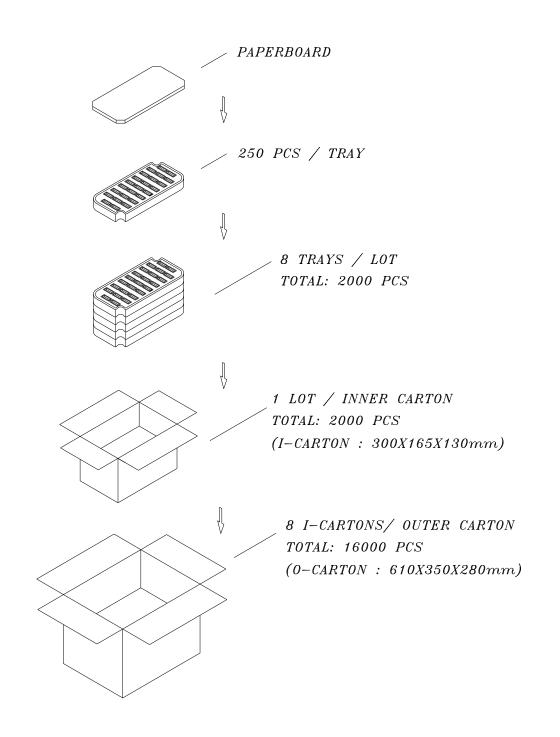


Fig.6 Spatial Distribution



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### 6. Packing Specification







## Through Hole Lamp LTL-R42M12NH51

### 7. CAUTIONS

#### 7.1. Application

This LED lamp is good for application of indoor and outdoor sign, also ordinary electronic equipment.

#### 7.2. Storage

The storage ambient for the LEDs should not exceed 30°C temperature or 70% relative humidity. It is recommended that LEDs out of their original packaging are used within three months. For extended storage out of their original packaging, it is recommended that the LEDs be stored in a sealed container with appropriate desiccant or in desiccators with nitrogen ambient.

#### 7.3. Cleaning

Use alcohol-based cleaning solvents such as isopropyl alcohol to clean the LEDs if necessary.

#### 7.4. Lead Forming & Assembly

During lead forming, the leads should be bent at a point at least 3mm from the base of LED lens. Do not use the base of the lead frame as a fulcrum during forming. Lead forming must be done before soldering, at normal temperature. During assembly on PCB, use minimum clinch force possible to avoid excessive mechanical stress.

#### 7.5. Soldering

When soldering, leave a minimum of 2mm clearance from the base of the lens/Holder to the soldering point. Dipping the lens/Holder into the solder must be avoided. Do not apply any external stress to the lead frame during soldering while the LED is at high temperature.

#### **Recommended soldering conditions:**

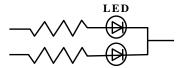
	Soldering iron	Wave soldering			
Temperature Soldering time Position	350°C Max. 3 seconds Max. (one time only) No closer than 2mm from the base of the epoxy bulb	Pre-heat Pre-heat time Solder wave Soldering time Dipping Position	120°C Max. 100 seconds Max. 260°C Max. 5 seconds Max. No lower than 2mm from the base of the epoxy bulb		

Note: Excessive soldering temperature and/or time might result in deformation of the LED lens or catastrophic failure of the LED.

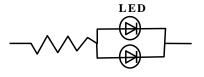
#### 7.6. Drive Method

An LED is a current-operated device. In order to ensure intensity uniformity on multiple LEDs connected in parallel in an application, it is recommended that a current limiting resistor be incorporated in the drive circuit, in series with each LED as shown in Circuit A below.





Circuit model (B)



(A) Recommended circuit

(B) The brightness of each LED might appear different due to the differences in the I-V characteristics of those LEDs.



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## Through Hole Lamp LTL-R42M12NH51

#### 7.7. ESD (Electrostatic Discharge)

#### Static Electricity or power surge will damage the LED. Suggestions to prevent ESD damage:

- Use a conductive wrist band or anti- electrostatic glove when handling these LEDs
- All devices, equipment, and machinery must be properly grounded
- Work tables, storage racks, etc. should be properly grounded
- Use ion blower to neutralize the static charge which might have built up on surface of the LEDs plastic lens as a result of friction between LEDs during storage and handing

Suggested checking list:

#### **Training and Certification**

7.7.1.1. Everyone working in a static-safe area is ESD-certified?

7.7.1.2. Training records kept and re-certification dates monitored?

#### Static-Safe Workstation & Work Areas

7.7.2.1. Static-safe workstation or work-areas have ESD signs?

- 7.7.2.2. All surfaces and objects at all static-safe workstation and within 1 ft measure less than 100V?
- 7.7.2.3. All ionizer activated, positioned towards the units?
- 7.7.2.4. Each work surface mats grounding is good?

#### **Personnel Grounding**

- 7.7.3.1. Every person (including visitors) handling ESD sensitive (ESDS) items wear wrist strap, heel strap or conductive shoes with conductive flooring?
- 7.7.3.1. If conductive footwear used, conductive flooring also present where operator stand or walk?
- 7.7.3.2. Garments, hairs or anything closer than 1 ft to ESD items measure less than 100V\*?
- 7.7.3.3. Every wrist strap or heel strap/conductive shoes checked daily and result recorded for all DLs?
- 7.7.3.4. All wrist strap or heel strap checkers calibration up to date?

Note: \*50V for Blue LED.

#### **Device Handling**

7.7.4.1. Every ESDS items identified by EIA-471 labels on item or packaging?

- 7.7.4.2. All ESDS items completely inside properly closed static-shielding containers when not at static-safe workstation?
- 7.7.4.3. No static charge generators (e.g. plastics) inside shielding containers with ESDS items?
- 7.7.4.4. All flexible conductive and dissipative package materials inspected before reuse or recycle?

#### Others

- 7.7.5.1. Audit result reported to entity ESD control coordinator?
- 7.7.5.2. Corrective action from previous audits completed?
- 7.7.5.3. Are audit records complete and on file?





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## Through Hole Lamp LTL-R42M12NH51

### 8. Reliability Test

Classification	assification Test Item Test Co		Sample Size	Reference Standard	
	Operation Life	Ta = Under Room Temperature IF= Per Data Sheet Maximum Rating Test Time= 1000hrs	22 PCS (CL=90%; LTPD=10%)	MIL-STD-750D:1026 (1995) MIL-STD-883G:1005 (2006)	
Endurance	High Temperature High Humidity storage	Ta = 60°C RH = 90% Test Time= 240hrs	22 PCS (CL=90%; LTPD=10%)	MIL-STD-202G:103B (2002) JEITA ED-4701:100 103 (2001)	
Test	High Temperature Storage	Ta= 105 ± 5°C Test Time= 1000hrs	22 PCS (CL=90%; LTPD=10%)	MIL-STD-750D:1031 (1995) MIL-STD-883G:1008 (2006) JEITA ED-4701:200 201 (2001)	
	Low Temperature Storage	Ta= -55 ± 5°C Test Time= 1000hrs	22 PCS (CL=90%; LTPD=10%)	JEITA ED-4701:200 202 (2001)	
	Temperature Cycling	$100^{\circ}$ C ~ $25^{\circ}$ C ~ $-40^{\circ}$ C ~ $25^{\circ}$ C 30mins 5mins 30mins 5mins Test time: 30 Cycles	22 PCS (CL=90%; LTPD=10%)	MIL-STD-750D:1051 (1995) MIL-STD-883G:1010 (2006) JEITA ED-4701:100 105 (2001) JESD22-A104C (2005)	
	Thermal Shock	100 ± 5°C ∼ -30°C ± 5°C 15mins 15mins Test time: 30 Cycles	22 PCS (CL=90%; LTPD=10%)	MIL-STD-750D:1056 (1995) MIL-STD-883G:1011 (2006) MIL-STD-202G:107G (2002) JESD22-A106B (2004)	
Environmental Test	Solder Resistance	T.sol = $260 \pm 5^{\circ}$ C Dwell Time= $10\pm1$ seconds 3mm from the base of the epoxy bulb	11 PCS (CL=90%; LTPD=18.9%)	MIL-STD-750D:2031(1995) JEITA ED-4701: 300 302 (2001)	
	Solderability	T. sol = $245 \pm 5^{\circ}$ C Dwell Time= $5 \pm 0.5$ seconds (Lead Free Solder, Coverage $\geq 95\%$ of the dipped surface)	11 PCS (CL=90%; LTPD=18.9%)	MIL-STD-750D:2026 (1995) MIL-STD-883G:2003 (2006) MIL-STD-202G:208H (2002) IPC/EIA J-STD-002 (2004)	
	Soldering Iron	T. sol = $350 \pm 5^{\circ}$ C Dwell Time= 3.5 ± 0.5 seconds	11 PCS (CL=90%; LTPD=18.9%)	MIL-STD-202G:208H (2002) JEITA ED-4701:300 302 (2001)	

### 9. Others

The appearance and specifications of the product may be modified for improvement, without prior notice

