LITEON®
OPTOELECTRONICS

Through Hole Lamp
Product Data Sheet
LTL710SGAJ2H274Y

Spec No. :DS20-2021-0063
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LITE-ON DCC

RELEASE

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LITEON OPTOELECTRONICS

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Rev	Description	By	Date
P01	Preliminary Specification (RDR-20210774-1)	Tina JH Chen	9/8/2021
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-	NPPR Received and Upload on OPNC.	Chalerm Y.	09/10/2021

LITEON® OPTOELECTRONICS

Data Sheet

Through Hole Lamp
LTL710SGAJ2H274Y

1. Description

CBI (Circuit Board Indicator) is a black plastic right angle Holder (Housing) which mates with Lite-On LED lamps. Lite-On CBI is available in a wide variety of packages, including top-view (Spacer) or right angle and horizontal or vertical arrays which is stackable and easy to assembly.

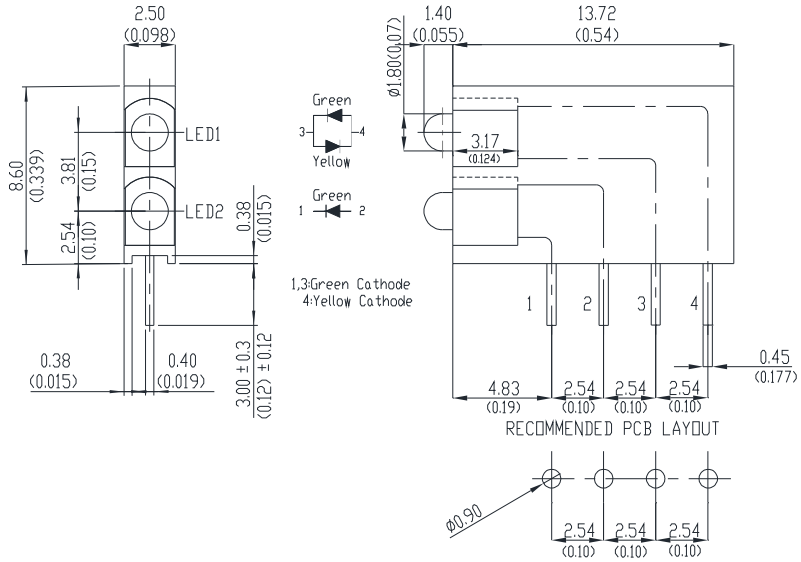
1. 1. Features

- Designed for ease in circuit board assembly.
- Halogen free product (Cl<900ppm, Br<900ppm; Cl+Br<1500ppm).
- Low power consumption & High efficiency.
- Lead free product & RoHS Compliant.
- Source color are AlInGaP green (yellow green) 570nm and yellow 589nm chips；AlInGaP green (yellow green) 570nm chips.

1.2. Applications

- Computer
- Communication
- Consumer
- Industrial

2. Outline Dimensions



Notes :

1. All dimensions are in millimeters (inches).
2. Tolerance is ±0.25mm (.010") unless otherwise noted.
3. The Holder (Housing) material is PA9T black, UL94V-0.
4. LED1 is green (yellow green)/yellow bi-color with white diffused lens；LED2 is green (yellow green) with whited diffused lens.
5. Specifications are subject to change without notice.

3. Absolute Maximum Ratings at TA=25℃

Parameter	LED1		LED2	Unit
	Green (Yellow Green)	Yellow	Green (Yellow Green)	
Power Dissipation	52	52	52	mW
Peak Forward Current (Duty Cycle≦1/10, Pulse Width≦10μs)	60	60	60	mA
DC Forward Current	20	20	20	mA
Operating Temperature Range	-30℃ to + 85℃			
Storage Temperature Range	-40℃ to + 100℃			
Lead Soldering Temperature [2.0mm (.079") From Body]	260℃ for 5 Seconds Max.			

LITEON OPTOELECTRONICS

Data Sheet

Through Hole Lamp
LTL710SGAJ2H274Y

4. Electrical / Optical Characteristics at TA=25℃

Parameter	Symbol	LED	Color	Min.	Typ.	Max.	Unit	Test Condition
Luminous Intensity	IV	LED1	Green		8.7		mcd	IF=20mA,,Note 1,4
			Yellow		5.6			
		LED2	Green		8.7			
Viewing Angle	2θ1/2	LED1	Green		70		deg	Note 2 (Fig.6)
			Yellow		70			
		LED2	Green		70			
Peak Emission Wavelength	λP	LED1	Green		572		nm	Measurement @Peak (Fig.1)
			Yellow		592			
		LED2	Green		572			
Dominant Wavelength	λd	LED1	Green	566	570	576	nm	IF=20 mA IF=10mA, Note 3
			Yellow	584	589	594		
		LED2	Green	566	570	576		
Spectral Line Half-Width	Δλ	LED1	Green		25		nm	
			Yellow		25			
		LED2	Green		25			
Forward Voltage	VF	LED1	Green	1.6	2.1	2.6	V	IF=20mA IF=10mA
			Yellow	1.6	2.1	2.6		
		LED2	Green	1.6	2.1	2.6		
Reverse Current	IR	LED1	Green			10	μA	VR = 5V, Note 6
			Yellow					
		LED2	Green			10		

NOTE:

1. Luminous intensity is measured with a light sensor and filter combination that approximates the CIE eye-response curve.
2. θ1/2 is the off-axis angle at which the luminous intensity is half the axial luminous intensity.
3. The dominant wavelength, λd is derived from the CIE chromaticity diagram and represents the single wavelength which defines the color of the device.
4. Iv guarantee must be included with ±30% testing tolerance.
5. Reverse current is controlled by dice source.
6. Reverse voltage (VR) condition is applied for IR test only. The device is not designed for reverse operation.

5. Typical Electrical / Optical Characteristics Curves

(25℃ Ambient Temperature Unless Otherwise Noted)

LED1(Yellow Green / Yellow)

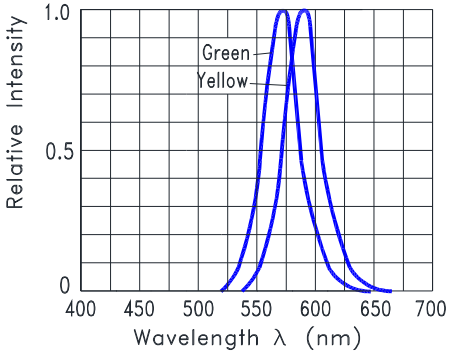


Fig.1 Relative Intensity VS. Wavelength

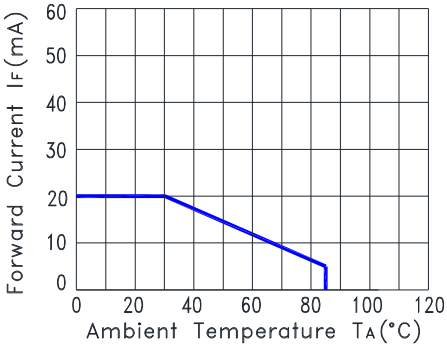


Fig.2 Forward Current Derating Curve

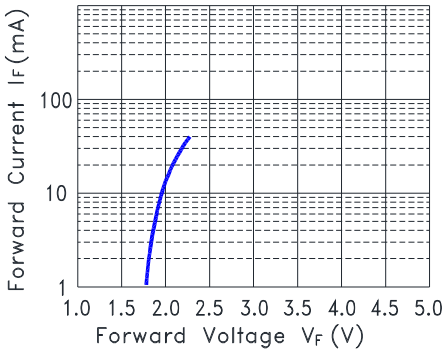


Fig.3 Forward Current vs. Forward Voltage

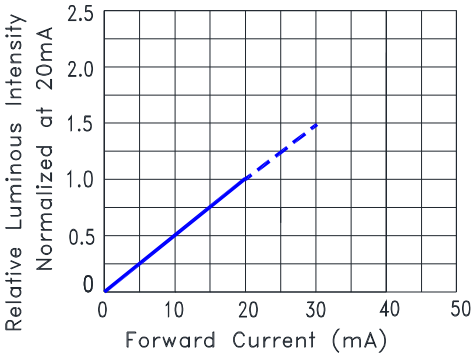


Fig.4 Relative Luminous Intensity vs. Forward Current

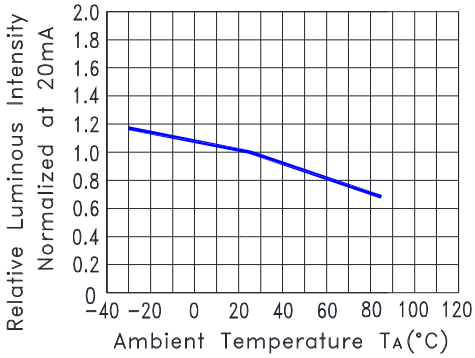


Fig.5 Relative Luminous Intensity VS. Ambient Temperature

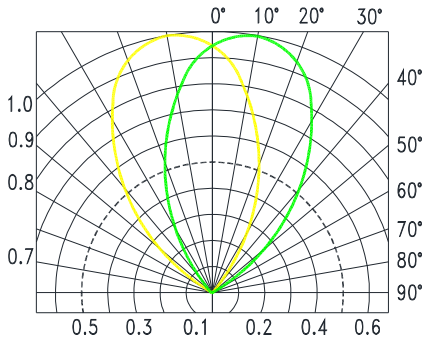


Fig.6 Spatial Distribution

LITEON OPTOELECTRONICS

Data Sheet

Through Hole Lamp
LTL710SGAJ2H274Y

LED2 (Yellow Green)

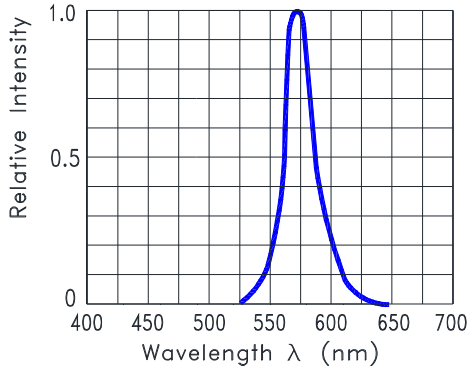


Fig.1 Relative Intensity VS. Wavelength

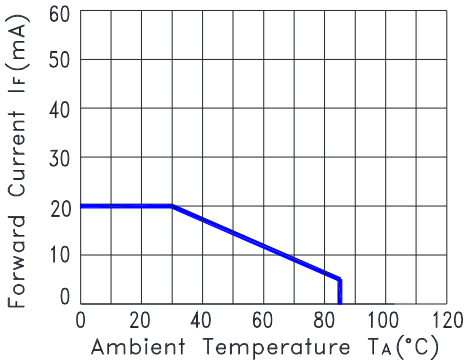


Fig.2 Forward Current Derating Curve

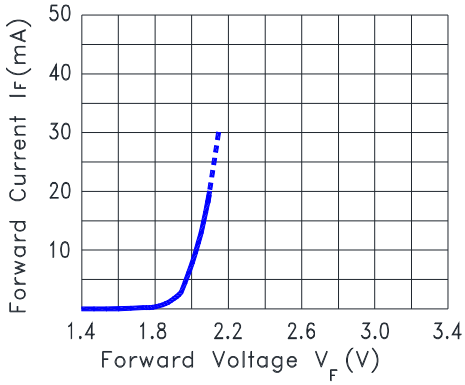


Fig.3 Forward Current vs. Forward Voltage

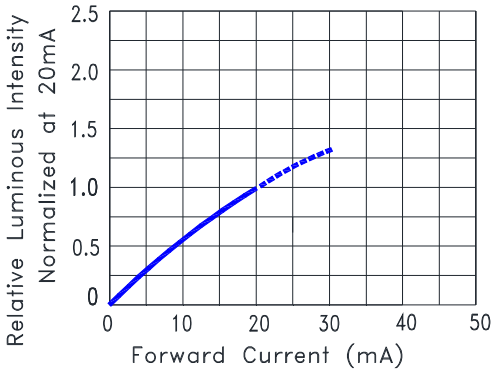


Fig.4 Relative Luminous Intensity vs. Forward Current

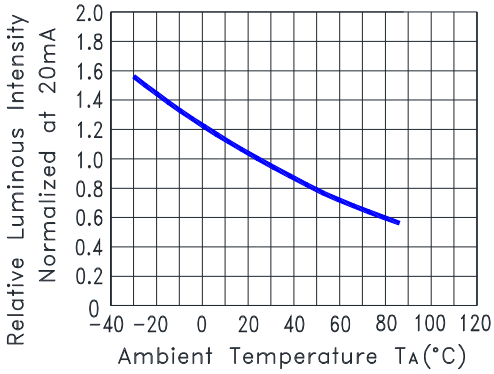


Fig.5 Relative Luminous Intensity VS. Ambient Temperature

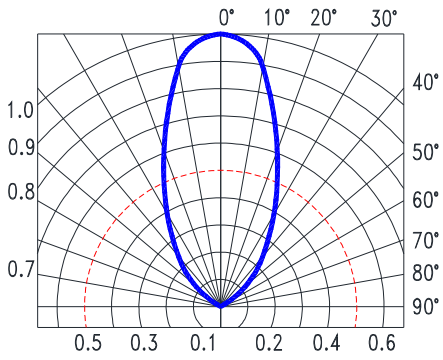
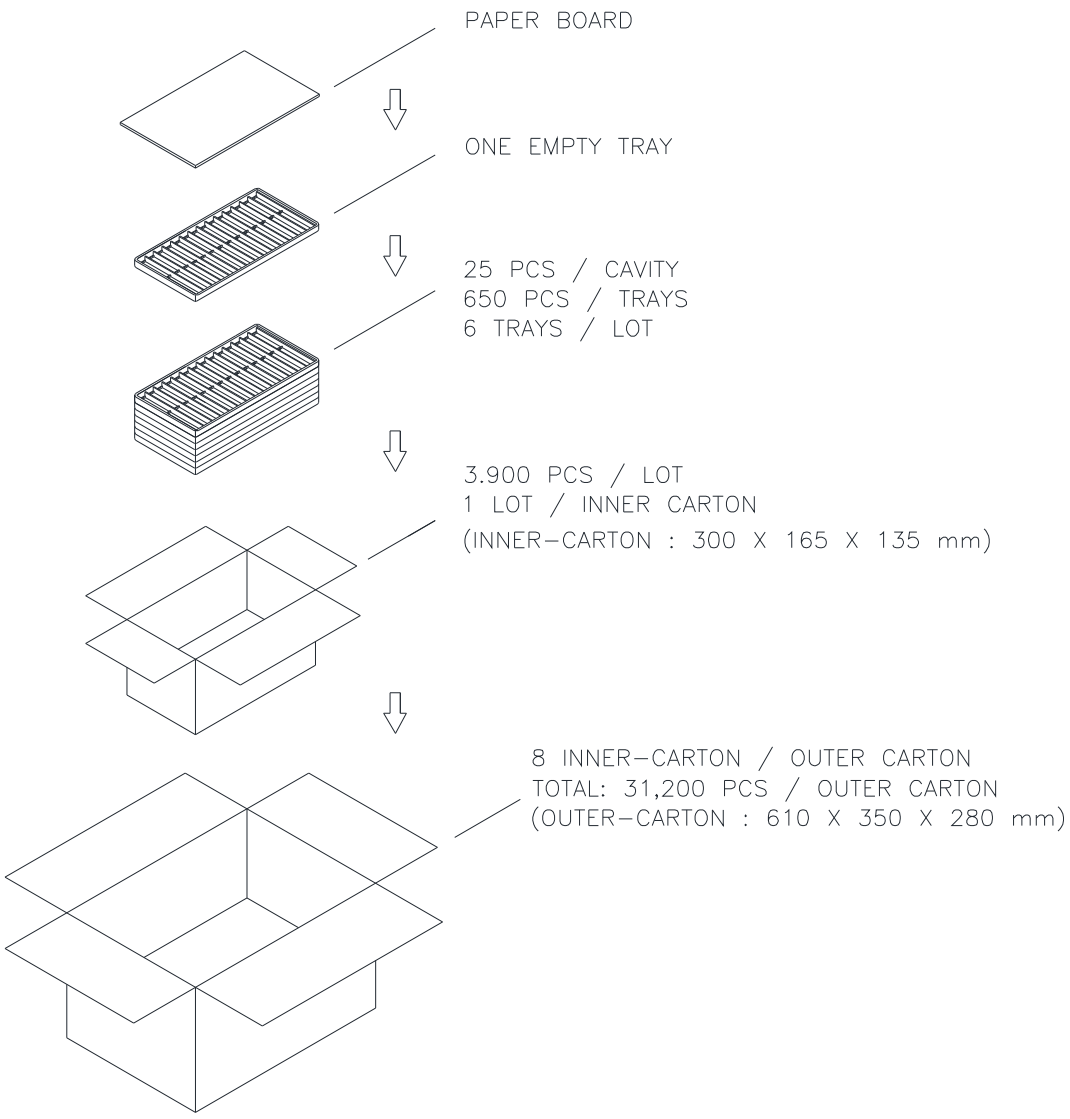


Fig.6 Spatial Distribution

6. Packing Specification



7. CAUTIONS

7.1. Application

This LED lamp is good for application of indoor and outdoor sign, also ordinary electronic equipment.

7.2. Storage

The storage ambient for the LEDs should not exceed 30℃ temperature or 70% relative humidity. It is re commended that LEDs out of their original packaging are used within three months. For extended storage out of their original packaging, it is recommended that the LEDs be stored in a sealed container with appropriate desiccant or in desiccators with nitrogen ambient.

7.3. Cleaning

Use alcohol-based cleaning solvents such as isopropyl alcohol to clean the LEDs if necessary.

7.4. Lead Forming & Assembly

During lead forming, the leads should be bent at a point at least 3mm from the base of LED lens. Do not use the base of the lead frame as a fulcrum during forming. Lead forming must be done before soldering, at normal temperature. During assembly on PCB, use minimum clinch force possible to avoid excessive mechanical stress.

7.5. Soldering

When soldering, leave a minimum of 2mm clearance from the base of the lens/Holder to the soldering point. Dipping the lens/Holder into the solder must be avoided. Do not apply any external stress to the lead frame during soldering while the LED is at high temperature.

Recommended soldering conditions:

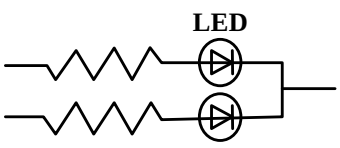
Soldering iron		Wave soldering	
Temperature	350℃ Max.	Pre-heat	120℃ Max.
Soldering time	3 seconds Max. (one time only)	Pre-heat time	100 seconds Max.
		Solder wave	260℃ Max.
Position	No closer than 2mm from the base of the epoxy bulb	Soldering time	5 seconds Max.
		Dipping Position	No lower than 2mm from the base of the epoxy bulb

Note: Excessive soldering temperature and/or time might result in deformation of the LED lens or catastrophic failure of the LED. IR reflow is not suitable process for through-hole type LED lamp product. Max temperature of wave soldering is not means that Holder's HDT/Melting temperature.

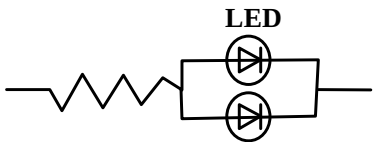
7.6. Drive Method

An LED is a current-operated device. In order to ensure intensity uniformity on multiple LEDs connected in parallel in an application, it is recommended that a current limiting resistor be incorporated in the drive circuit, in series with each LED as shown in Circuit A below.

Circuit model (A)



Circuit model (B)



(A) Recommended circuit
(B) The brightness of each LED might appear different due to the differences in the I-V characteristics of those LEDs.

7.7. ESD (Electrostatic Discharge)

Static Electricity or power surge will damage the LED.

Suggestions to prevent ESD damage:

- Use a conductive wrist band or anti- electrostatic glove when handling these LEDs
- All devices, equipment, and machinery must be properly grounded
- Work tables, storage racks, etc. should be properly grounded
- Use ion blower to neutralize the static charge which might have built up on surface of the LEDs plastic lens as a result of friction between LEDs during storage and handing

Suggested checking list:

Training and Certification

8.7.1.1. Everyone working in a static-safe area is ESD-certified?
8.7.1.2. Training records kept and re-certification dates monitored?

Static-Safe Workstation & Work Areas

8.7.2.1. Static-safe workstation or work-areas have ESD signs?
8.7.2.2. All surfaces and objects at all static-safe workstation and within 1 ft measure less than 100V?
8.7.2.3. All ionizer activated, positioned towards the units?
8.7.2.4. Each work surface mats grounding is good?

Personnel Grounding

8.7.3.1. Every person (including visitors) handling ESD sensitive (ESDS) items wear wrist strap, heel strap or conductive shoes with conductive flooring?
8.7.3.1. If conductive footwear used, conductive flooring also present where operator stand or walk?
8.7.3.2. Garments, hairs or anything closer than 1 ft to ESD items measure less than 100V*?
8.7.3.3. Every wrist strap or heel strap/conductive shoes checked daily and result recorded for all DLs?
8.7.3.4. All wrist strap or heel strap checkers calibration up to date?
Note: *50V for Blue LED.

Device Handling

8.7.4.1. Every ESDS items identified by EIA-471 labels on item or packaging?
8.7.4.2. All ESDS items completely inside properly closed static-shielding containers when not at static-safe workstation?
8.7.4.3. No static charge generators (e.g. plastics) inside shielding containers with ESDS items?
8.7.4.4. All flexible conductive and dissipative package materials inspected before reuse or recycle?

Others

8.7.5.1. Audit result reported to entity ESD control coordinator?
8.7.5.2. Corrective action from previous audits completed?
8.7.5.3. Are audit records complete and on file?

LITEON OPTOELECTRONICS

Data Sheet

Through Hole Lamp
LTL710SGAJ2H274Y

8. Reliability Test

Classification	Test Item	Test Condition	Sample Size	Reference Standard
Endurance Test	Operation Life	Ta = Under room temperature IF = per datasheet maximum drive current Test Time= 1000hrs	22 PCS (CL=90%; LTPD=10%)	MIL-STD-750D:1026 (1995) MIL-STD-883G:1005 (2006)
	High Temperature High Humidity storage	Ta = 60℃ RH = 90% Test Time= 240hrs	22 PCS (CL=90%; LTPD=10%)	MIL-STD-202G:103B (2002) JEITA ED-4701:100 103 (2001)
	High Temperature Storage	Ta= 105 ± 5℃ Test Time= 1000hrs	22 PCS (CL=90%; LTPD=10%)	MIL-STD-750D:1031 (1995) MIL-STD-883G:1008 (2006) JEITA ED-4701:200 201 (2001)
	Low Temperature Storage	Ta= -55 ± 5℃ Test Time= 1000hrs	22 PCS (CL=90%; LTPD=10%)	JEITA ED-4701:200 202 (2001)
Environmental Test	Temperature Cycling	100℃ ～ 25℃ ～ -40℃ ～ 25℃ 30mins 5mins 30mins 5mins 30 Cycles	22 PCS (CL=90%; LTPD=10%)	MIL-STD-750D:1051 (1995) MIL-STD-883G:1010 (2006) JEITA ED-4701:100 105 (2001) JESD22-A104C (2005)
	Thermal Shock	100 ± 5℃ ～ -30℃ ± 5℃ 15mins 15mins 30 Cycles (<20 secs transfer)	22 PCS (CL=90%; LTPD=10%)	MIL-STD-750D:1056 (1995) MIL-STD-883G:1011 (2006) MIL-STD-202G:107G (2002) JESD22-A106B (2004)
	Solder Resistance	T.sol = 260 ± 5℃ Dwell Time= 10±1 seconds 3mm from the base of the epoxy bulb	11 PCS (CL=90%; LTPD=18.9%)	MIL-STD-750D:2031(1995) JEITA ED-4701: 300 302 (2001)
	Solderability	T. sol = 245 ± 5℃ Dwell Time= 5 ± 0.5 seconds (Lead Free Solder, Coverage ≧95% of the dipped surface)	11 PCS (CL=90%; LTPD=18.9%)	MIL-STD-750D:2026 (1995) MIL-STD-883G:2003 (2006) MIL-STD-202G:208H (2002) IPC/EIA J-STD-002 (2004)
	Soldering Iron	T. sol = 350 ± 5℃ Dwell Time= 3.5 ± 0.5 seconds	11 PCS (CL=90%; LTPD=18.9%)	MIL-STD-202G:208H (2002) JEITA ED-4701:300 302 (2001)

9. Others

The appearance and specifications of the product may be modified for improvement, without prior notice.