



Product Data Sheet

LTPL-A138DWAGB

Spec No. :DS33-2018-0006
Effective Date: 08/30/2018
Revision: -

LITE-ON DCC

RELEASE

BNS-OD-FC001/A4

CSP LTPL-A138DWAGB

1. Description

LITEON FLASH is a compact power LED light source specifically designed to provide greater amounts of light where needed, enabling higher resolution pictures to be taken in lower level ambient light environments at greater distances..

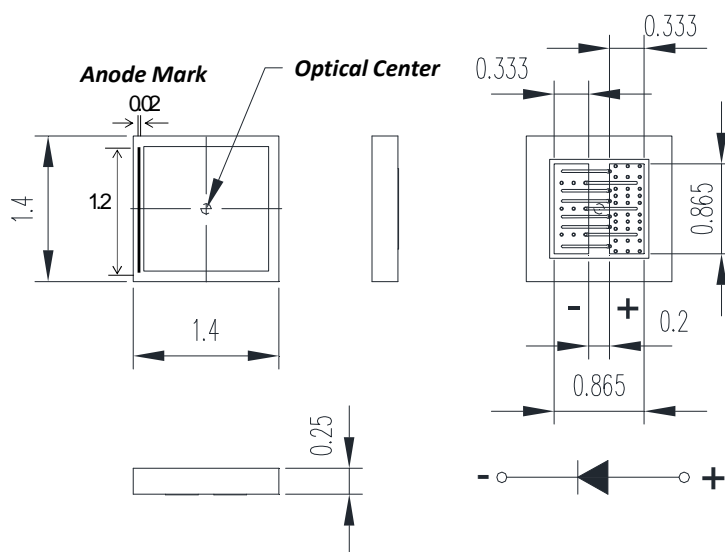
1.1 Features

- Smallest and highest flux density chip scale package
- Direct attach – Flip Chip type
- High efficacy and flux at high current density
- Better thermal conduction

1.2 Applications

- Camera phones
- Handhelds
- Digital still cameras

2. Package Dimensions



Part No.	Lens Color	Emitted Color
LTPL-A138DWAGB	Orange/White	InGaN White

Notes :

1. All dimensions are in millimeters and dimension tolerances are $\pm 0.075\text{mm}$.
2. Dimensions without tolerances are for reference only.

CSP
LTPL-A138DWAGB

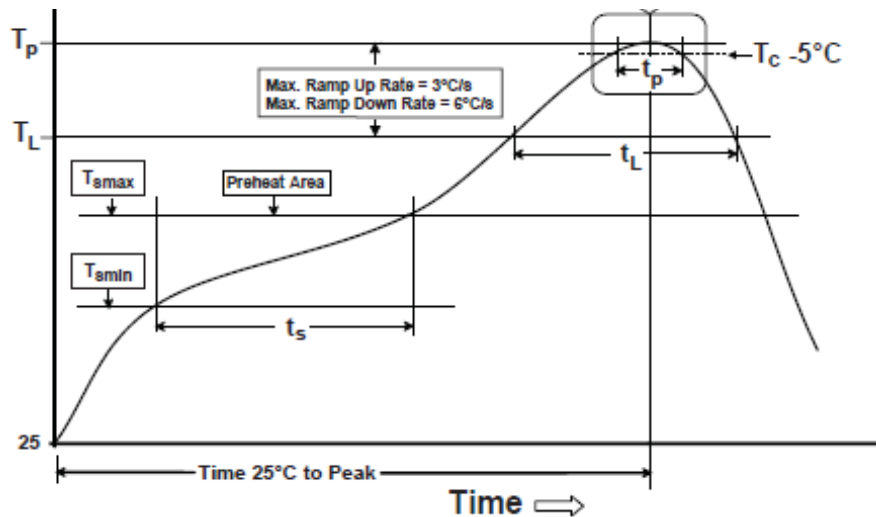
3. Rating and Characteristics

3.1 Absolute Maximum Ratings at Ta=25°C

Parameter	LTPL-A138DWAGB	Unit
Power Dissipation(Pulse Mode)	5.7	W
Pulsed Forward Current (400ms:ON , 3600ms: OFF , D=0.1)	1500	mA
DC Forward Current	350	mA
Junction Temperature	125	°C
Operating Temperature Range	-40 °C to + 85 °C	
Storage Temperature Range	-40 °C to + 100 °C	

CSP LTPL-A138DWAGB

3.2 Suggest IR Reflow Condition For Pb Free Process:



Profile Feature	Lead Free Assembly
Average Ramp-Up Rate (T_{Smax} to T_P)	$3^\circ C / \text{second max}$
Preheat Temperature Min (T_{Smin})	$150^\circ C$
Preheat Temperature Max (T_{Smax})	$200^\circ C$
Preheat Time (t_{Smin} to t_{Smax})	60 – 120 seconds
Time Maintained Above Temperature (T_L)	$217^\circ C$
Time Maintained Above Time (t_L)	60 – 150 seconds
Peak / Classification Temperature (T_P)	$250^\circ C$
Time Within $5^\circ C$ of Actual Peak Temperature (t_p)	5 seconds
Ramp – Down Rate	$6^\circ C / \text{second max}$
Time $25^\circ C$ to Peak Temperature	8 minutes max

Notes:

1. All temperatures refer to topside of the package, measured on the package body surface.
2. The soldering condition referring to J-STD-020D.
3. The soldering profile could be further referred to different soldering grease material characteristic. The grease vendor will provide this information.
4. A rapid-rate process is not recommended for the LEDs cooling down from the peak temperature.
5. Although the recommended reflow conditions are specified above, the reflow or hand soldering condition at the lowest possible temperature is desirable for the LEDs.
6. LiteOn cannot make a guarantee on the LEDs which have been already assembled using the dip soldering method.
7. When flux is used, it should be a halogen and Pb free flux. Ensure that the manufacturing process is not designed in a manner where the flux will come in contact with the LEDs.
8. Make sure that there are no issues with the type and amount of solder that is being used.

CSP LTPL-A138DWAGB

3.3 Electrical / Optical Characteristics at Ta=25°C

Parameter	Symbol	LTPL-A138DWAGB			Unit	Test Condition
		MIN.	TYP.	MAX.		
Luminous Flux	Φ_V	180	240	280	lm	$I_{FP} = 1000\text{mA}$
Viewing Angle	$2\theta_{1/2}$		120		Deg	Fig.2
Color Temperature	CCT	4000	--	5000	K	$I_{FP} = 1000\text{mA}$
Color Rendering index	CRI	80	--	--		$I_{FP} = 1000\text{mA}$
Forward Voltage	VF1	2.9	3.2	3.8	V	$I_{FP} = 1000\text{mA}$
Forward Voltage	VF2	2.0	--	--		$I_{FP} = 10\ \mu\text{A}$
Reverse Current	IR			100	μA	VR = 5V Note 4

NOTE:

1. Luminous flux measurement tolerance: $\pm 10\%$
2. Forward voltage measurement tolerance: $\pm 0.1\text{V}$
3. Electric and optical data is tested at 300ms pulse condition.
4. Reverse voltage(VR) condition is applied to IR test only. The device is not designed for reverse operation.
5. Electric and optical data are reference only.

CSP LTPL-A138DWAGB

4. Bin Rank

4.1 Bin code list

■ Luminous Flux Binning

Parameter	Bin	Symbol	Min	Typ.	Max	Unit	Condition
Luminous Flux	N0	ΦV	180	--	250	lm	$I_{FP} = 1000\text{mA}$
	P1		250	--	280		

Luminous flux measurement tolerance: $\pm 10\%$

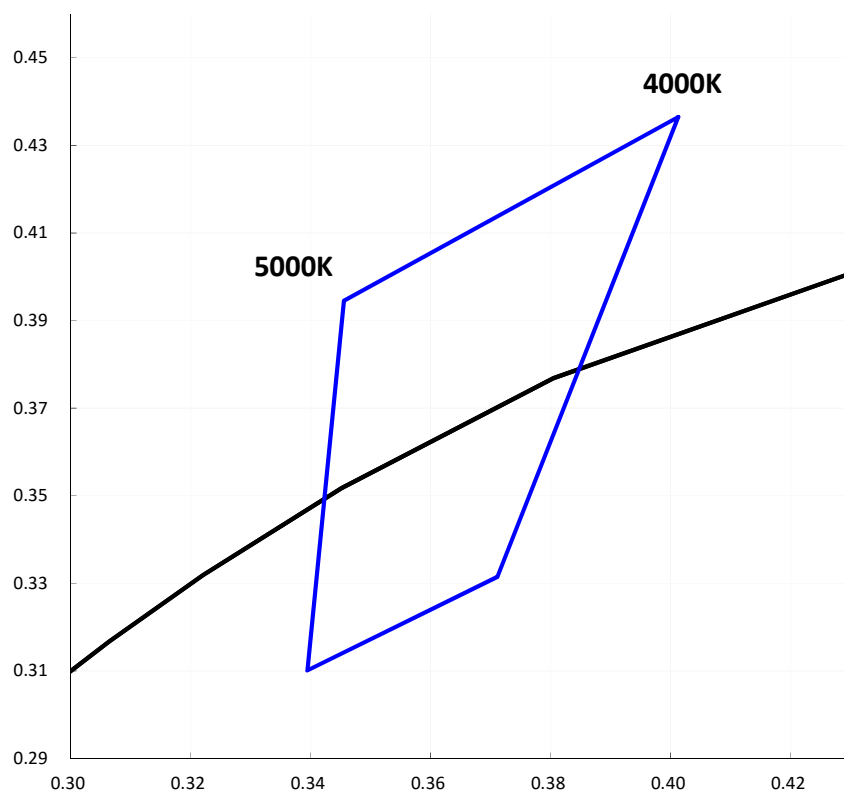
■ Forward Voltage Binning

Parameter	Bin	Symbol	Min	Typ.	Max	Unit	Condition
Forward Voltage	4	VF	2.9	--	3.8	V	$I_{FP} = 1000\text{mA}$

Forward voltage measurement tolerance: $\pm 0.1\text{V}$

CSP LTPL-A138DWAGB

■ Category Code Table



Bin	Cx	Cy
-	0.4013	0.4365
	0.3456	0.3945
	0.3395	0.3101
	0.3712	0.3315

Notes:

1. The value is all dies operated performance.
2. The chromaticity coordinates (x, y) is derived from the CIE 1931 chromaticity diagram.
3. IS CAS140B is for the luminous flux (lm) and the CIE1931 chromaticity coordinates (x, y) testing. The chromaticity coordinates (x, y) guarantee should be added ± 0.01 tolerance.

CSP
LTPL-A138DWAGB

5. Typical Electrical / Optical Characteristics Curves.

(25°C Ambient Temperature Unless Otherwise Noted)

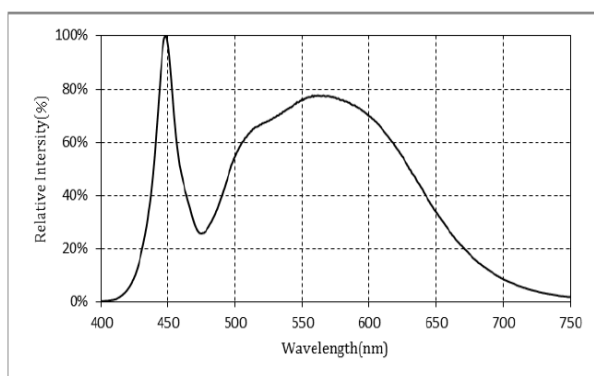


Fig 1. Relative Spectrum of Emission

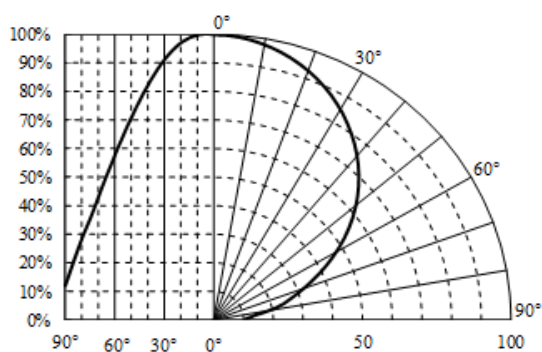


Fig 2. Radiation Characteristics

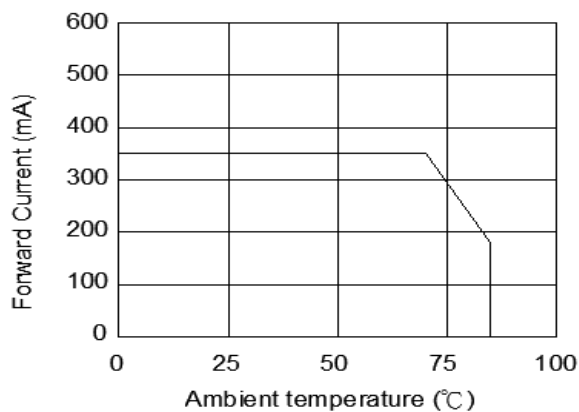


Fig 3. Forward Current Derating Curve

CSP
LTPL-A138DWAGB

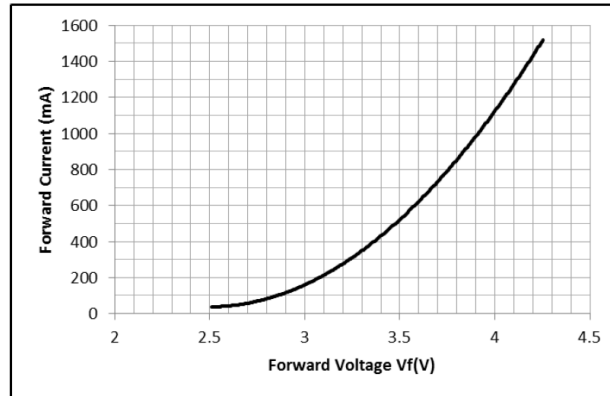


Fig 4. Forward Current

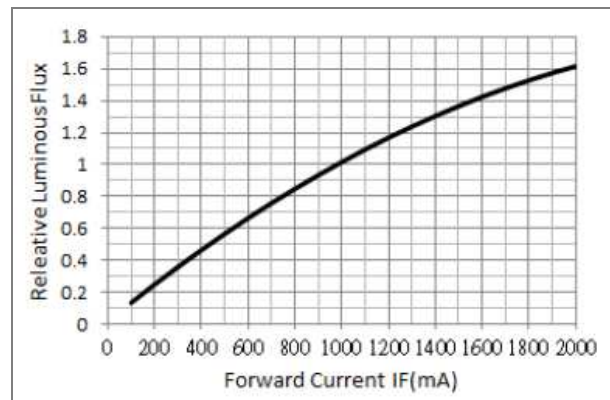


Fig 5. Relative Luminous Flux

Notes: All correlation data is mounted on thermal heat sink with 2cmX 2cm Metal Core PCB

CSP LTPL-A138DWAGB

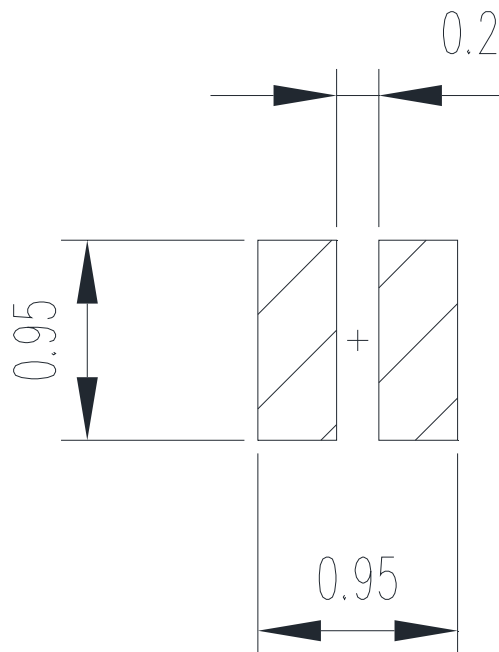
6. User Guide

6.1 Cleaning

Do not use unspecified chemical liquid to clean LED they could harm the package.

If clean is necessary, immerse the LED in ethyl alcohol or in isopropyl alcohol at normal temperature for less one minute.

6.2 Recommend Printed Circuit Board Attachment Pad



Notes:

1. Suggest stencil thickness is maximum 0.10mm.

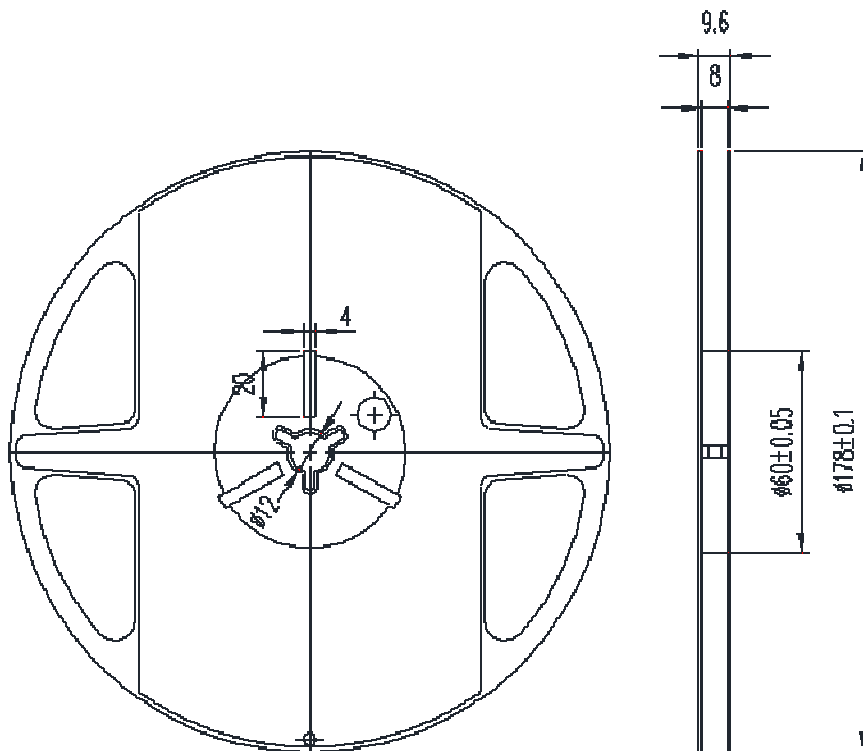
6.3 Package Dimensions Of Tape And Reel



1. Drawings not to scale.
2. All dimensions are in millimeters.
3. All dimensions without tolerances are for reference only.

CSP LTPL-A138DWAGB

6.4 Package Dimensions of Reel



Notes:

1. Drawings not to scale.
2. All dimensions are in millimeters.
3. All dimensions without tolerances are for reference only.
4. Minimum package quantity is 500 pieces for remainders.
5. 7 inch reel-6000 pieces.
6. Empty component pockets sealed with top cover tape.
7. The maximum number of consecutive missing lamps is two
8. In accordance with EIA-481 specifications

CSP LTPL-A138DWAGB

7. Cautions

7.1 Application

The LEDs described here are intended to be used for ordinary electronic equipment (such as office equipment, communication equipment and household applications). Consult Liteon's Sales in advance for information on applications in which exceptional reliability is required, particularly when the failure or malfunction of the LEDs may directly jeopardize life or health (such as in aviation, transportation, traffic control equipment, medical and life support systems and safety devices).

7.2 Storage

This product is qualified as Moisture sensitive Level 3 per JEDEC J-STD-020 Precaution when handling this moisture sensitive product is important to ensure the reliability of the product.

The package is sealed:

The LEDs should be stored at 30°C or less and 90%RH or less. And the LEDs are limited to use within one year, while the LEDs is packed in moisture-proof package with the desiccants inside.

The package is opened:

The LEDs should be stored at 30°C or less and 60%RH or less. Moreover, the LEDs are limited to solder process within 168hrs. If the Humidity Indicator shows the pink color in 10% even higher or exceed the storage limiting time since opened, that we recommended to baking LEDs at 60°C at least 48hrs. To seal the remainder LEDs return to package, it's recommended to be with workable desiccants in original package.

7.3 Cleaning

Use alcohol-based cleaning solvents such as isopropyl alcohol to clean the LED if necessary.

7.4 Soldering

Recommended soldering conditions:

Reflow soldering		Soldering iron	
Pre-heat	150~200°C	Temperature	300°C Max.
Pre-heat time	120 sec. Max.	Soldering time	3 sec. Max.
Peak temperature	260°C Max.		(one time only)
Soldering time	10 sec. Max.(Max. two times)		

Notes:

Because different board designs use different number and types of devices, solder pastes, reflow ovens, and circuit boards, no single temperature profile works for all possible combinations. However, you can successfully mount your packages to the PCB by following the proper guidelines and PCB-specific characterization.

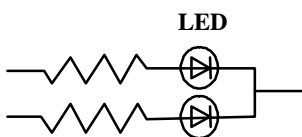
LITE-ON Runs both component-level verification using in-house **KYRAMX98** reflow chambers and board-level assembly. The results of this testing are verified through post-reflow reliability testing. Profiles used at LITE-ON are based on JEDEC standards to ensure that all packages can be successfully and reliably surface mounted. Figure on page2 shows a sample temperature profile compliant to JEDEC standards. You can use this example as a generic target to set up your reflow process. You should adhere to the JEDEC profile limits as well as specifications and recommendations from the solder paste manufacturer to avoid damaging the device and create a reliable solder joint.

CSP LTPL-A138DWAGB

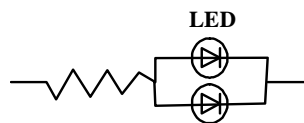
7.5 Drive Method

A LED is a current-operated device. In order to ensure intensity uniformity on multiple LEDs connected in parallel in an application, it is recommended that a current limiting resistor be incorporated in the drive circuit, in series with each LED as shown in Circuit A below.

Circuit model A



Circuit model B



(A) Recommended circuit.

(B) The brightness of each LED might appear different due to the differences in the I-V characteristics of those LEDs.

7.6 ESD (Electrostatic Discharge)

Static Electricity or power surge will damage the LED.

Suggestions to prevent ESD damage:

- Use of a conductive wrist band or anti-electrostatic glove when handling these LEDs.
- All devices, equipment, and machinery must be properly grounded.
- Work tables, storage racks, etc. should be properly grounded.
- Use ion blower to neutralize the static charge which might have built up on surface of the LED's plastic lens as a result of friction between LEDs during storage and handling.

Plastic lens as a result of friction between LEDs during storage and handling.

The Vf of "good" LEDs should be $>2.0V @ 0.1mA$ for InGaN product. forward voltage, or "no lightup" at low currents.

To verify for ESD damage, check for "lightup" and VF of the suspect LEDs at low currents.

7.7 Handling Precautions

Do not handle the LEDs with bare hands as it will contaminate the LED surface and may affect the optical characteristics: it might cause the LEDs to be deformed and the bump to break, which will cause the LED not to light up.

- When handling the LEDs with tweezers, it should be use special tweezers (e.g. vacuum nozzle) and hold the LEDs by the top surface; no more than 5N should be applied to the LED. If excessive force is applied to the LEDs, it may cause the resin to be damaged, chipped, delaminated and deformed. This may cause the internal connections to be damaged and the LED no to illuminate.
- During processing, mechanical stress on the surface should be minimized as much as possible.
- Do not stack assembled PCBs together. Failure to comply can cause the resin portion of product to be cut, chipped, delaminated and deform. It may cause bump to break, leading to catastrophic failures.

CSP LTPL-A138DWAGB

8. Reliability Test

Stress Test	Stress Condition	Stress Duration
Thermal Cycling	- 40°C (30min)/100°C (30min) transition time 5 min@25°C	500 cycles
Thermal Shock	- 40°C (20min)/100°C (20min) transition time 20 secs	200 cycles
Room Temperature Operation Life	25°C, IF=350mA	500hr
Low Temperature Storage Life	- 40°C	500hr
High Temperature Storage Life	100°C	500hr
Resistance to solder heat	T _{sol} =260°C, 10sec, 6min	3 times
Pulse test	I _F =1000mA 50ms on /950 ms off @25°C	30000times

Notes:

- All reliability items are mounted on thermal heat sink with 2cmX 2cm Metal Core PCB
The thermal resistance R_{th}=9.5°C/W (from T_j to PCB)



9. Others

The appearance and specifications of the product may be modified for improvement without prior notice.

10. Suggested Checking List

Training and Certification

- Everyone working in a static-safe area is ESD-certified?
- Training records kept and re-certification dates monitored?

Static-Safe Workstation & Work Areas

- Static-safe workstation or work-areas have ESD signs?
- All surfaces and objects at all static-safe workstation and within 1 ft measure less than 100V?
- All ionizer activated, positioned towards the units?
- Each work surface mats grounding is good?

Personnel Grounding

- Every person (including visitors) handling ESD sensitive (ESDS) items wears wrist strap, heel strap or conductive shoes with conductive flooring?
- If conductive footwear used, conductive flooring also present where operator stand or walk?
- Garments, hairs or anything closer than 1 ft to ESD items measure less than 100V*?

CSP LTPL-A138DWAGB

4. Every wrist strap or heel strap/conductive shoes checked daily and result recorded for all DLs?
5. All wrist strap or heel strap checkers calibration up to date?

Note: *50V for InGaN LED.

Device Handling

1. Every ESDS items identified by EIA-471 labels on item or packaging?
2. All ESDS items completely inside properly closed static-shielding containers when not at static-safe workstation?
3. No static charge generators (e.g. plastics) inside shielding containers with ESDS items?
4. All flexible conductive and dissipative package materials inspected before reuse or recycles?

Others

1. Audit result reported to entity ESD control coordinator?
2. Corrective action from previous audits completed?
3. Are audit records complete and on file?