



Product Data Sheet

LTW-A031AWAYN

Spec No. :DS33-2021-0005
Effective Date: 03/18/2021
Revision: -

LITE-ON DCC

RELEASE

BNS-OD-FC001/A4

CSP LTW-A031AWAYN

1. Description

CSP LED from Lite-On are available in miniature sizes and special configurations for automated PC board assembly and space-sensitive applications. These CSP LED are suitable for use in a wide variety of electronic equipment, including cordless and cellular phones, notebook computers, network systems, home appliances, and indoor signboard applications..

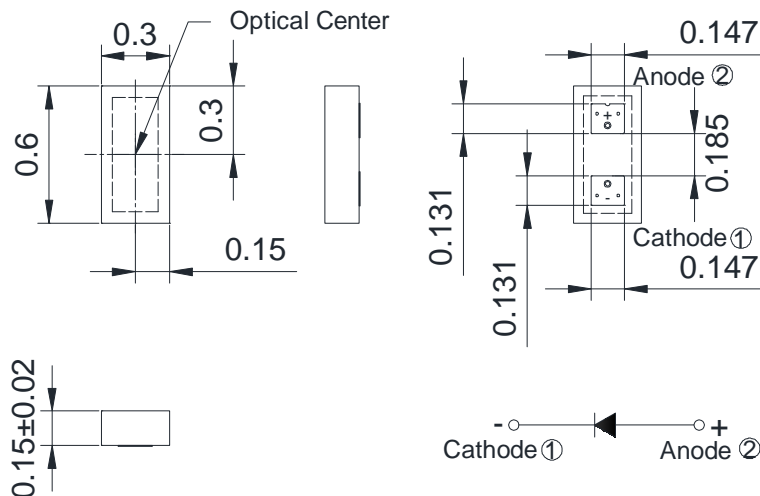
1.1 Features

- Smallest and highest flux density chip scale package
- Direct attach – Flip Chip type
- High efficacy and flux at high current density
- Better thermal conduction

1.2 Applications

- Telecommunication, Office automation, home appliances, industrial equipment
- Keypad/Keyboard Backlighting
- Status indicator
- Micro-displays
- Signal and Symbol Luminary

2. Package Dimensions



Part No.	Lens Color	Emitted Color
LTW-A031AWAYN	Yellow/White	InGaN White

Notes :

1. All dimensions are in millimeters and dimension tolerances are $\pm 0.05\text{mm}$.
2. Dimensions without tolerances are for reference only.

CSP LTW-A031AWAYN

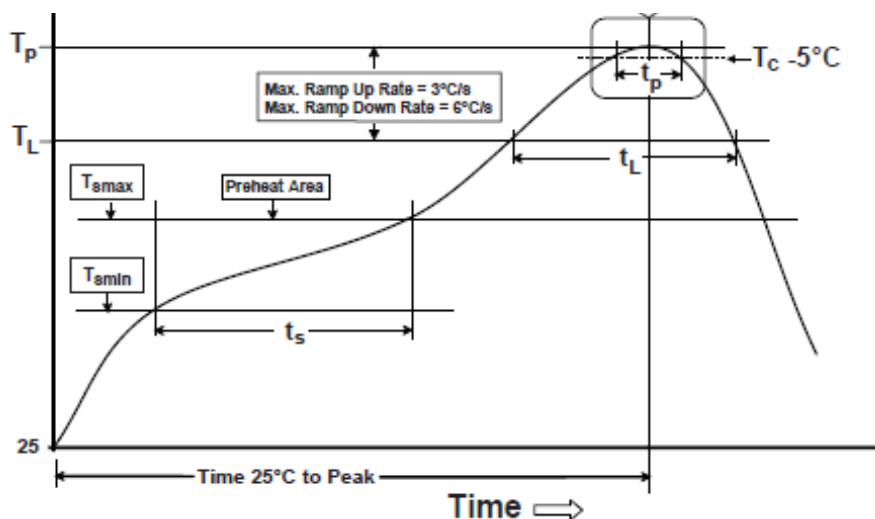
3. Rating and Characteristics

3.1 Absolute Maximum Ratings at Ta=25°C

Parameter	LTW-A031AWAYN	Unit
Power Dissipation(Pulse Mode)	72	mW
Pulsed Forward Current (400ms:ON , 3600ms: OFF , D=0.1)	50	mA
DC Forward Current	20	mA
Operating Temperature Range	-40 °C to + 85 °C	
Storage Temperature Range	-40 °C to + 85 °C	

CSP LTW-A031AWAYN

3.2 Suggest IR Reflow Condition For Pb Free Process:



Profile Feature	Lead Free Assembly
Average Ramp-Up Rate (T_{Smax} to T_P)	3°C / second max
Preheat Temperature Min (T_{Smin})	150°C
Preheat Temperature Max (T_{Smax})	200°C
Preheat Time (t_{Smin} to t_{Smax})	60 – 120 seconds
Time Maintained Above Temperature (T_L)	217°C
Time Maintained Above Time (t_L)	60 – 150 seconds
Peak / Classification Temperature (T_P)	250°C
Time Within 5°C of Actual Peak Temperature (t_p)	5 seconds
Ramp – Down Rate	6°C / second max
Time 25°C to Peak Temperature	8 minutes max

Notes:

1. All temperatures refer to topside of the package, measured on the package body surface.
2. The soldering condition referring to J-STD-020D.
3. The soldering profile could be further referred to different soldering paste material characteristic. The solder paste vendor will provide this information.
4. A rapid-rate process is not recommended for the LEDs cooling down from the peak temperature.
5. Although the recommended reflow conditions are specified above, the reflow or hand soldering condition at the lowest possible temperature is desirable for the LEDs.
6. LiteOn cannot make a guarantee on the LEDs which have been already assembled using the dip soldering method.
7. When flux is used, it should be a halogen and Pb free flux. Ensure that the manufacturing process is not designed in a manner where the flux will come in contact with the LEDs.
8. Make sure that there are no issues with the type and amount of solder that is being used.

CSP LTW-A031AWAYN

3.3 Electrical / Optical Characteristics at Ta=25°C

Parameter	Symbol	LTW-A031AWAYN			Unit	Test Condition
		MIN.	TYP.	MAX.		
Luminous Intensity	IV	40	60	100	mcd	IF = 1mA Note 1,5
Peak Emission Wavelength	λP	445	452	455	nm	IF = 1mA (Fig. 1)
Viewing Angle	$2\theta_{1/2}$	138			deg	(Fig.5)
Chromaticity Coordinates	x		0.31			IF = 1mA Note 3
	y		0.31			
Forward Voltage	VF	2.5	-	2.9	V	IF = 1mA Note 2,5
Reverse Current	IR	0	-	10	μA	VR=5V Note 4

NOTE:

1. Luminous Intensity measurement tolerance: $\pm 10\%$
2. Forward voltage measurement tolerance: $\pm 0.1V$
3. Electric and optical data is tested at 300ms pulse condition.
4. Reverse voltage(VR) condition is applied to IR test only. The device is not designed for reverse operation.
5. Electric and optical data are reference only.

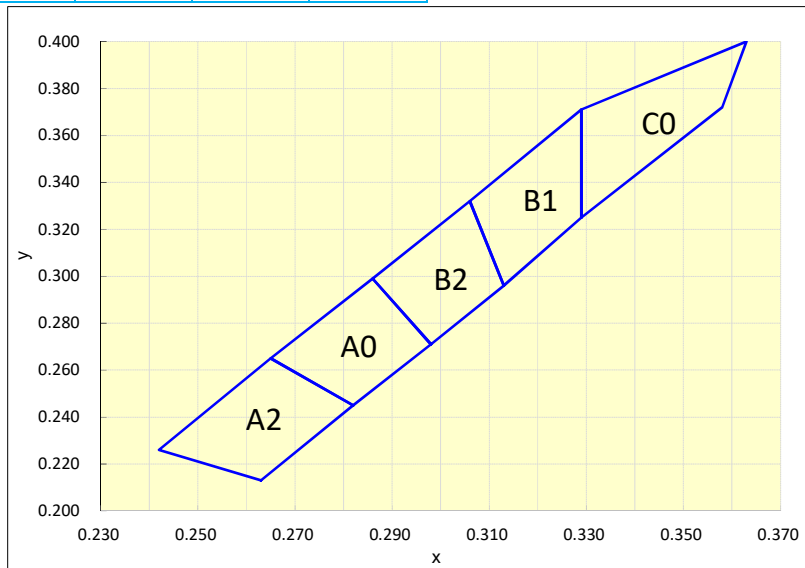
CSP LTW-A031AWAYN

4. Bin Rank

4.1 Bin code list

■ Category Code Table

Color Ranks Table											
Ranks	Color bin limits at IF = 1mA					Ranks	Color bin limits at IF = 1mA				
	CIE 1931Chromaticity coordinates						CIE 1931Chromaticity coordinates				
A2	x	0.263	0.282	0.265	0.242	B1	x	0.313	0.329	0.329	0.306
	y	0.213	0.245	0.265	0.226		y	0.296	0.325	0.371	0.332
A0	x	0.282	0.298	0.286	0.265	C0	x	0.329	0.358	0.363	0.329
	y	0.245	0.271	0.299	0.265		y	0.325	0.372	0.400	0.371
B2	x	0.298	0.313	0.306	0.286						
	y	0.271	0.296	0.332	0.299						

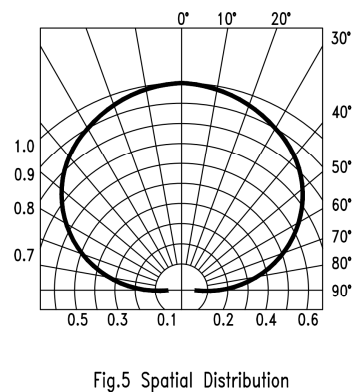
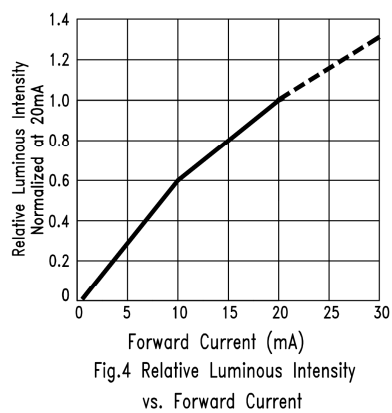
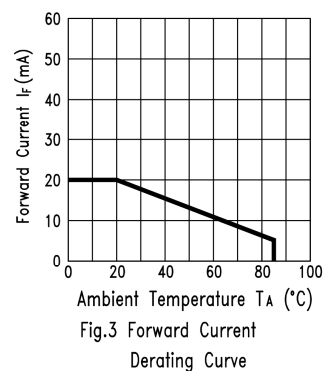
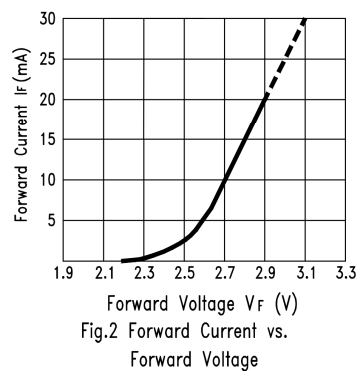
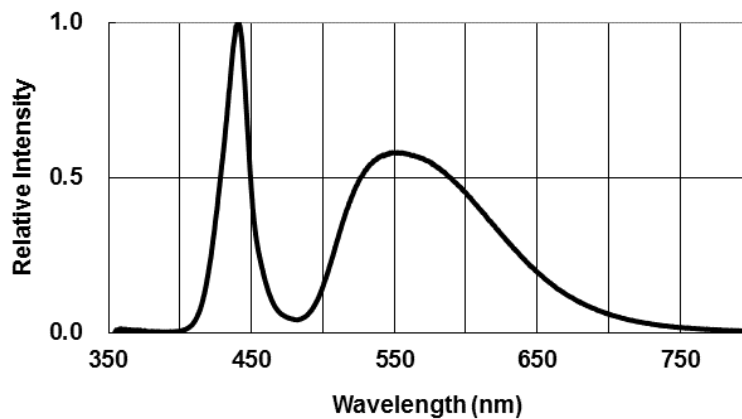


Notes:

1. The value is all dies operated performance.
2. The chromaticity coordinates (x, y) is derived from the CIE 1931 chromaticity diagram.
3. IS CAS140B is for the luminous Intensity (mcd) and the CIE1931 chromaticity coordinates (x, y) testing. The chromaticity coordinates (x, y) guarantee should be added ± 0.01 tolerance.

CSP LTW-A031AWAYN

(25°C Ambient Temperature Unless Otherwise Noted)



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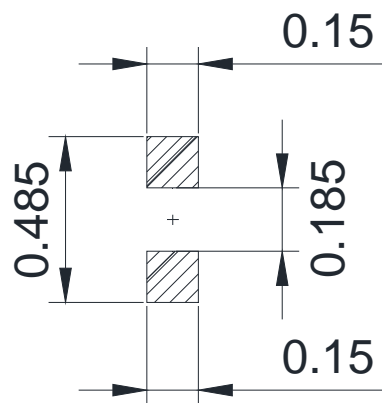
6. User Guide

6.1 Cleaning

Do not use unspecified chemical liquid to clean LED they could harm the package.

If clean is necessary, immerse the LED in ethyl alcohol or in isopropyl alcohol at normal temperature for less one minute.

6.2 Recommend Printed Circuit Board Attachment Pad

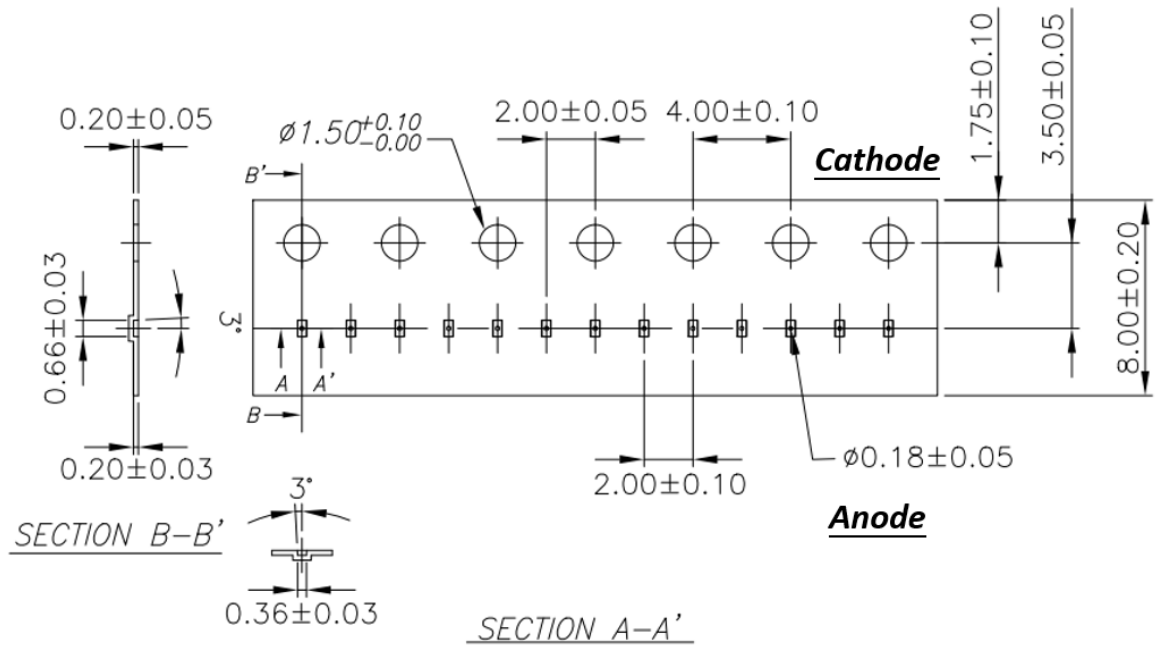


Notes:

1. Suggest stencil thickness is maximum 0.10mm.
2. Suggest between two solder pad gap dimension equal 0.185mm.

CSP
LTW-A031AWAYN

6.3 Package Dimensions of Tape And Reel

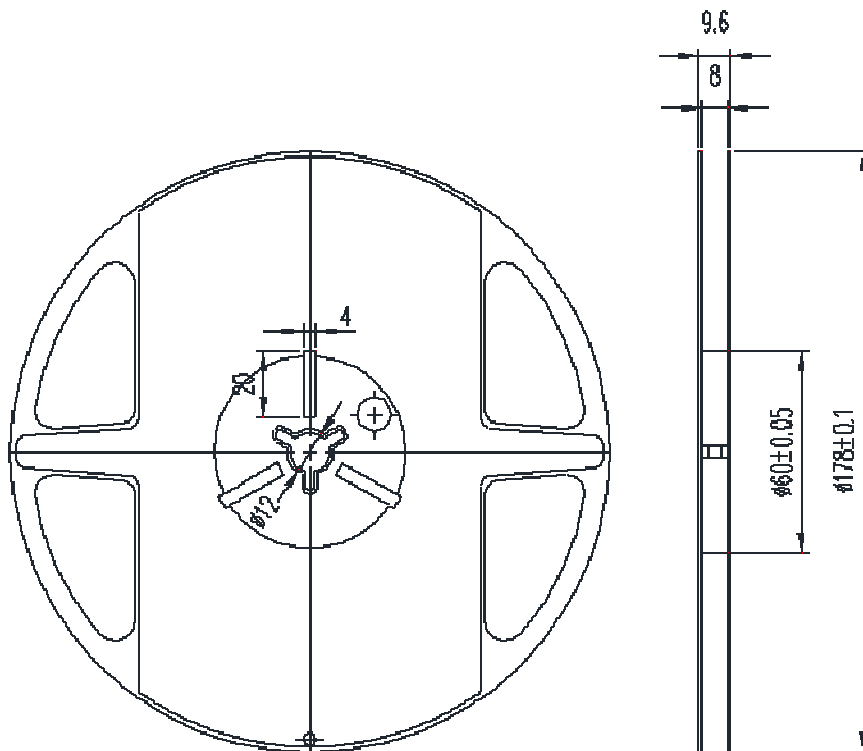


Note:

1. Drawings not to scale.
2. All dimensions are in millimeters.
3. All dimensions without tolerances are for reference only.

CSP LTW-A031AWAYN

6.4 Package Dimensions of Reel



Notes:

1. Drawings not to scale.
2. All dimensions are in millimeters.
3. All dimensions without tolerances are for reference only.
4. Minimum package quantity is 500 pieces for remainders.
5. 7 inch reel-6000 pieces.
6. Empty component pockets sealed with top cover tape.
7. The maximum number of consecutive missing lamps is two
8. In accordance with EIA-481 specifications

CSP LTW-A031AWAYN

7. Cautions

7.1 Application

The LEDs described here are intended to be used for ordinary electronic equipment (such as office equipment, communication equipment and household applications). Consult Liteon's Sales in advance for information on applications in which exceptional reliability is required, particularly when the failure or malfunction of the LEDs may directly jeopardize life or health (such as in aviation, transportation, traffic control equipment, medical and life support systems and safety devices).

7.2 Storage

This product is qualified as Moisture sensitive Level 3 per JEDEC J-STD-020 Precaution when handling this moisture sensitive product is important to ensure the reliability of the product.

The package is sealed:

The LEDs should be stored at 30°C or less and 90%RH or less. And the LEDs are limited to use within one year, while the LEDs is packed in moisture-proof package with the desiccants inside.

The package is opened:

The LEDs should be stored at 30°C or less and 60%RH or less. Moreover, the LEDs are limited to solder process within 168hrs. If the Humidity Indicator shows the pink color in 10% even higher or exceed the storage limiting time since opened, that we recommended to baking LEDs at 60°C at least 48hrs. To seal the remainder LEDs return to package, it's recommended to be with workable desiccants in original package.

7.3 Cleaning

Use alcohol-based cleaning solvents such as isopropyl alcohol to clean the LED if necessary.

7.4 Soldering

Recommended soldering conditions:

Reflow soldering		Soldering iron	
Pre-heat	150~200°C	Temperature	300°C Max.
Pre-heat time	120 sec. Max.	Soldering time	3 sec. Max.
Peak temperature	260°C Max.		(one time only)
Soldering time	10 sec. Max. (Max. two times)		

Notes:

Because different board designs use different number and types of devices, solder pastes, reflow ovens, and circuit boards, no single temperature profile works for all possible combinations. However, you can successfully mount your packages to the PCB by following the proper guidelines and PCB-specific characterization.

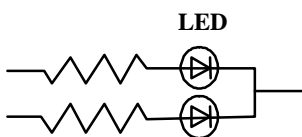
LITE-ON Runs both component-level verification using in-house **KYRAMX98** reflow chambers and board-level assembly. The results of this testing are verified through post-reflow reliability testing. Profiles used at LITE-ON are based on JEDEC standards to ensure that all packages can be successfully and reliably surface mounted. Figure on page2 shows a sample temperature profile compliant to JEDEC standards. You can use this example as a generic target to set up your reflow process. You should adhere to the JEDEC profile limits as well as specifications and recommendations from the solder paste manufacturer to avoid damaging the device and create a reliable solder joint.

CSP LTW-A031AWAYN

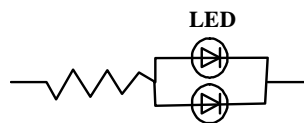
7.5 Drive Method

A LED is a current-operated device. In order to ensure intensity uniformity on multiple LEDs connected in parallel in an application, it is recommended that a current limiting resistor be incorporated in the drive circuit, in series with each LED as shown in Circuit A below.

Circuit model A



Circuit model B



(A) Recommended circuit.

(B) The brightness of each LED might appear different due to the differences in the I-V characteristics of those LEDs.

7.6 ESD (Electrostatic Discharge)

Static Electricity or power surge will damage the LED.

Suggestions to prevent ESD damage:

- Use of a conductive wrist band or anti-electrostatic glove when handling these LEDs.
- All devices, equipment, and machinery must be properly grounded.
- Work tables, storage racks, etc. should be properly grounded.
- Use ion blower to neutralize the static charge which might have built up on surface of the LED's plastic lens as a result of friction between LEDs during storage and handling.

Plastic lens as a result of friction between LEDs during storage and handling.

The V_f of "good" LEDs should be $>2.0V @ 0.1mA$ for InGaN product. forward voltage, or "no lightup" at low currents.

To verify for ESD damage, check for "lightup" and V_f of the suspect LEDs at low currents.

CSP LTW-A031AWAYN

8. Reliability Test

Classification	Test Item	Test Condition	Reference Standard
Endurance Test	Operation Life	Ta= Under Room Temperature As Per Data Sheet Maximum Rating *Test Time= 500HRS (-24HRS,+72HRS). @10mA	MIL-STD-750D:1026 MIL-STD-883D:1005 JIS C 7021:B-1
	High Temperature High Humidity Storage	Ta= 65±5°C,RH= 90~95% *Test Time= 240HRS±2HRS	MIL-STD-202F:103B JIS C 7021:B-11
	High Temperature Storage	Ta= 105±5 °C *Test Time= 500HRS (-24HRS,+72HRS)	MIL-STD-883D:1008 JIS C 7021:B-1
	Low Temperature Storage	Ta= -55±5 °C *Test Time=500HRS (-24HRS,+72HRS)	JIS C 7021:B-12
Environmental Test	Temperature Cycling	105 °C ~ 25 °C ~ -55 °C ~ 25 °C 30mins 5mins 30mins 5mins 10 Cycles	MIL-STD-202F:107D MIL-STD-750D:1051 MIL-STD-883D:1010 JIS C 7021:A-4
	Thermal Shock	85 ± 5 °C ~ -40 °C ± 5 °C 10mins 10mins 10Cycles	MIL-STD-202F:107D MIL-STD-750D:1051 MIL-STD-883D:1011
	Solder Resistance	T.sol= 260 ± 5 °C Dwell Time=10 ± 1secs	MIL-STD-202F:210A MIL-STD-750D:2031 JIS C 7021:B-1
	IR Reflow	Ramp-up rate(217 °C to Peak) +3 °C / second max Temp. maintain at 175(±25) °C 180 seconds max Temp. maintain above 217 °C 60-150 seconds Peak temperature range 260 °C +0/-5 °C Time within 5°C of actual Peak Temperature (tp) 10-30 seconds Ramp-down rate +6 °C /second max	MIL-STD-750D:2031.2 J-STD-020D
	Solder ability	T.sol= 235 ± 5 °C Immersion time 2±0.5 sec Immersion rate 25±2.5 mm/sec Coverage ≥ 95% of the dipped surface	MIL-STD-202F:208D MIL-STD-750D:2026 MIL-STD-883D:2003 IEC 68 Part 2-20 JIS C 7021:A-2

CSP LTW-A031AWAYN

9. Others

The appearance and specifications of the product may be modified for improvement without prior notice.

10. Suggested Checking List

Training and Certification

1. Everyone working in a static-safe area is ESD-certified?
2. Training records kept and re-certification dates monitored?

Static-Safe Workstation & Work Areas

1. Static-safe workstation or work-areas have ESD signs?
2. All surfaces and objects at all static-safe workstation and within 1 ft measure less than 100V?
3. All ionizer activated, positioned towards the units?
4. Each work surface mats grounding is good?

Personnel Grounding

1. Every person (including visitors) handling ESD sensitive (ESDS) items wears wrist strap, heel strap or conductive shoes with conductive flooring?
2. If conductive footwear used, conductive flooring also present where operator stand or walk?
3. Garments, hairs or anything closer than 1 ft to ESD items measure less than 100V*?
4. Every wrist strap or heel strap/conductive shoes checked daily and result recorded for all DLs?
5. All wrist strap or heel strap checkers calibration up to date?

Note: *50V for InGaN LED.

Device Handling

1. Every ESDS items identified by EIA-471 labels on item or packaging?
2. All ESDS items completely inside properly closed static-shielding containers when not at static-safe workstation?
3. No static charge generators (e.g. plastics) inside shielding containers with ESDS items?
4. All flexible conductive and dissipative package materials inspected before reuse or recycles?

Others

1. Audit result reported to entity ESD control coordinator?
2. Corrective action from previous audits completed?
3. Are audit records complete and on file?